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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patentee : Richard C. Foss, Peter B. Gillingham, Robert Harland, Masami Mitsuhashi,
Atsushi Wada

Patent No.: US RE40,552 E Issued: October 28, 2008

Title: DYNAMIC RANDOM ACCESS MEMORY USING IMPERFECT
ISOLATING TRANSISTORS

CERTIFICATE OF MAILING OR TRANSMISSION	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, or is being facsimile transmitted to the United States Patent and Trademark Office on:	
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of Correction

REQUEST FOR CERTIFICATE OF CORRECTION

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Sir:

Pursuant to 35 U.S.C. § 254, we hereby request a Certificate of Correction be issued for the above-referenced U.S. Letters Patent to correct errors as shown below:

On the front page of Patent at (63), insert before Continuation
--Continuation of Reissue Application No. 08/853,507, filed May 8, 1997, now RE37,641, issued April 9, 2002, which is a Reissue of U.S. Patent No. 5,414,662, issued from U.S. Application No. 08/147,038, filed November 4, 1993, which was a --.

In Claim 18, Column 10, line 7, insert -- access -- before the word devices.

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Since the errors were made by the U.S. Patent and Trademark Office and not by Patentee or Patentee's Attorney/Agent, it is understood that there are no additional fees for the requested Certificate of Correction. A Certificate of Correction is enclosed herewith.

Respectfully submitted,

HAMILTON, BROOK, SMITH & REYNOLDS, P.C.

By 

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Dated: 

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

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